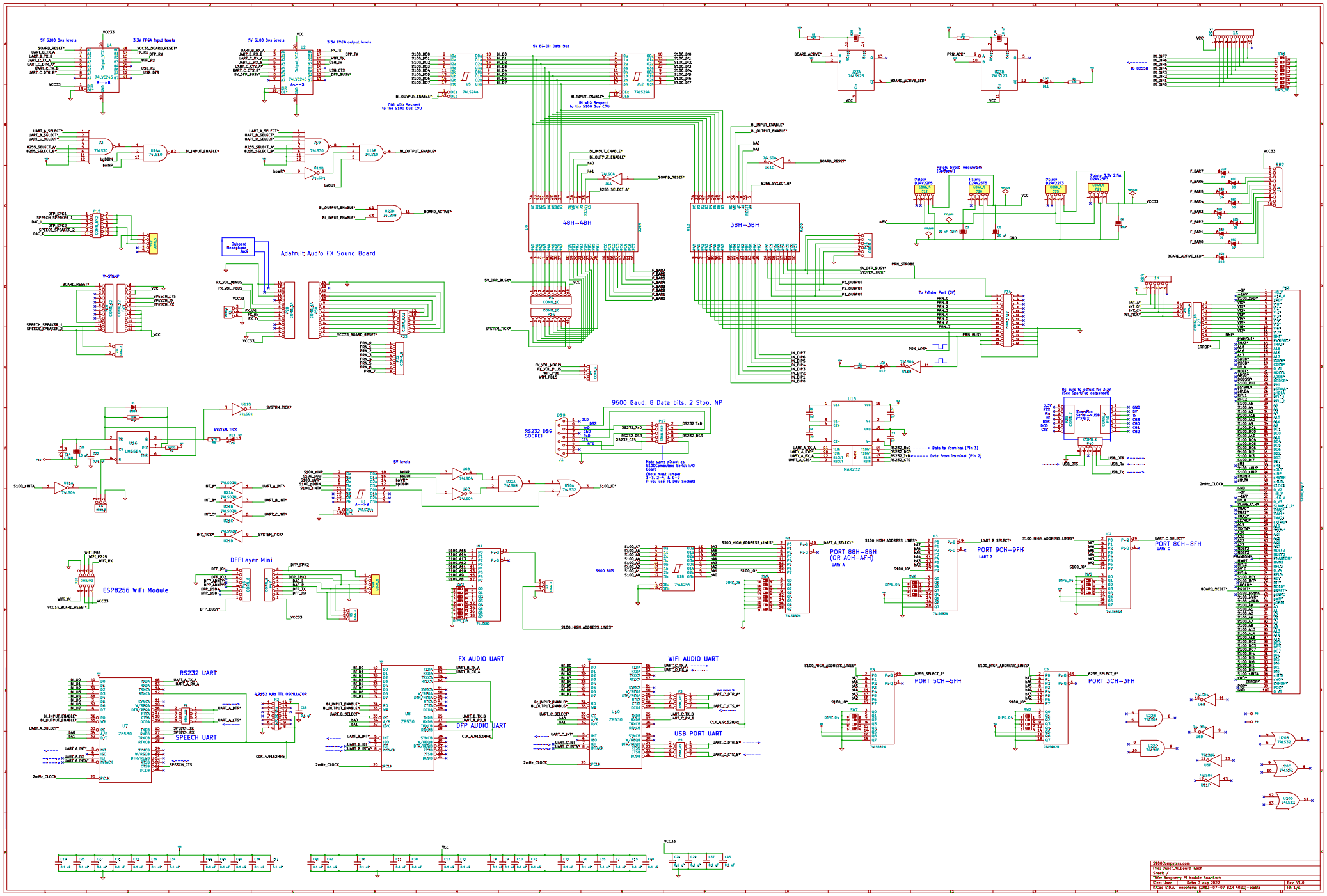

5V S100 Bus levels
3.3V FPGA input levels
5V S100 Bus levels
3.3V FPGA output levels
5V Bi-Dir Data Bus
Out with Reset to the S100 Bus CPU
In with Reset to the S100 Bus CPU
48H-4BH
38H-3BH
Adafruit Audio FX Sound Board
Onboard Headphone Jack
V-STAMP
9600 Baud, 8 Data bits, 2 Stop, NP
RS232 DB9 SOCKET
Note same pinout as S100Computers Serial I/O Board
(Note must jumper 1-3, 2-4, & D-D if you use it DB9 Socket)
MAX232
Data to Terminal (Pin 3)
Data from Terminal (Pin 2)
Be sure to adjust for 3.3V (See SparkFun datasheet)
To Printer Port (5V)
To 8255B
Palolo 5Volt Regulators (Optional)
Palolo 3.3V 2.5A D24V25F3
Palolo D24V22F5
Palolo D24V22F3
ESP8266 WiFi Module
DFPLayer Mini
S100 BUS
PORT 8BH-8BH (OR A0H-AFH)
PORT 9CH-9FH
PORT 8CH-8FH
RS232 UART
SPEECH UART
4.9152 MHz TTL OSCILLATOR
FX AUDIO UART
DFP AUDIO UART
WIFI AUDIO UART
USB PORT UART
PORT 5CH-5FH
PORT 3CH-3FH
S100Computers.com
File: Super_IO_Board II.sch
Sheet: /
Title: Raspberry PI Module Board.sch
Size: User
Date: 7 aug 2022
Rev: V1.0
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable
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