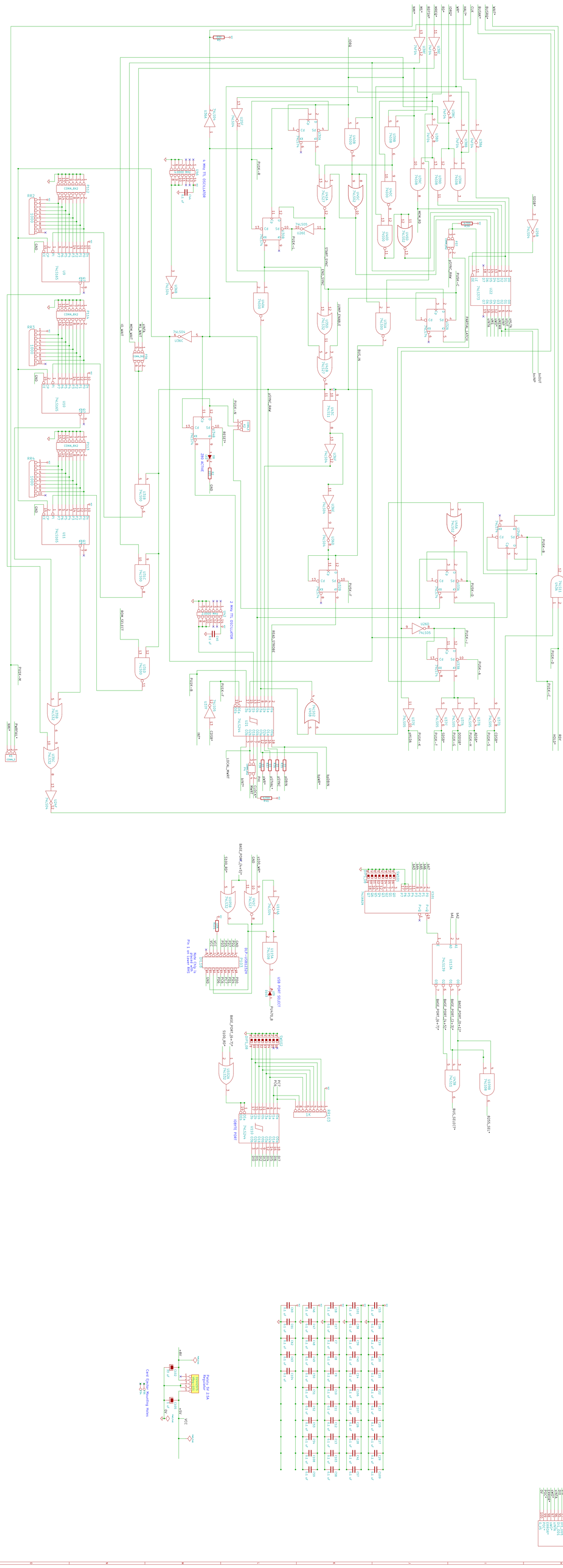
4 MHz TTL OSCILLATOR
2 MHz TTL OSCILLATOR
74LS04
74LS00
74LS32
74LS74
74LS373
74LS244
74LS245
74LS165
74LS157
74LS138
74LS139
74LS08
74LS86
74LS27
74LS11
74LS51
74LS10
74LS30
74LS05
CONN_8X2
CONN_3X2
CONN_2X2
CONN_4
CONN_5
CONN_7
RP2
RP3
RP4
RP5
RP6
RP7
RP8
RP9
RP10
RP11
RP12
RP13
1000
680
GND
VCC
+5V
+8V
-8V
-16.5V
+16.5V
PHI
RESET*
RESET
CLOCK*
MWR*
MRD*
IORQ*
WR*
RD*
M1*
MREQ*
IOREQ*
WAIT*
INT*
NMI*
HALT*
REFSH*
BUSAK*
BUSRQ*
SLAVE_CLR*
SLAVE_CLK*
PHANTOM*
ROM_SELECT*
RAM_SELECT*
MEMWR*
MEMRD*
IOWR*
IORD*
M1D*
RFSHD*
HALTD*
WAITD*
INTD*
NMID*
BUSRQD*
RESETD*
CLOCKD*
SYNCD*
WRD*
RDD*
IOWRA*
IORDA*
RESETA*
BUSACK*
SYNC*
SYNC_RAW
START_SYNC
END_SYNC
JUMP_ENABLE
PARTIAL_LATCH
READ_STROBE
MEM_WAIT
IO_WAIT
LOCAL_MWR*
LOCAL_MRD*
LOCAL_IOWR*
LOCAL_IORD*
LOCAL_INT*
LOCAL_NMI*
LOCAL_HALT*
LOCAL_WAIT*
LOCAL_BUSRQ*
LOCAL_RESET*
LOCAL_M1*
LOCAL_RFSH*
LOCAL_IORQ*
LOCAL_RD*
LOCAL_WR*
LOCAL_MREQ*
BOARD_RESET*
BOARD_RESET
ONBOARD ROM ENABLE (INSTALL JUMPER)
EXTERNAL ROM ENABLE (NO JUMPER)
ROM_SELECT*
RAM_SELECT*
MEMRD*
MEMWR*
IOWR*
IORD*
INTD*
NMID*
HALTD*
WAITD*
BUSRQD*
RESETD*
BUSACK*
SYNC*
SYNCD*
WRD*
RDD*
M1D*
RFSHD*
IOWRA*
IORDA*
RESETA*
CLOCKD*
SYNC_RAW
START_SYNC
END_SYNC
JUMP_ENABLE
PARTIAL_LATCH
READ_STROBE
MEM_WAIT
IO_WAIT
LOCAL_MWR*
LOCAL_MRD*
LOCAL_IOWR*
LOCAL_IORD*
LOCAL_INT*
LOCAL_NMI*
LOCAL_HALT*
LOCAL_WAIT*
LOCAL_BUSRQ*
LOCAL_RESET*
LOCAL_M1*
LOCAL_RFSH*
LOCAL_IORQ*
LOCAL_RD*
LOCAL_WR*
LOCAL_MREQ*
BOARD_RESET*
BOARD_RESET