

TITLE Signals for ISA bus on adaptor board.  
 PATTERN Non Latched  
 REVISION 2  
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 COMPANY  
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CHIP EXTRA PALCE22V10 ; Device not selected

----- PIN Declarations -----  
 ;----- INPUT SIGNALS -----  
 ;Not currently used

Pin 1 ISA\_INT  
 PIN 2 bpDBIN  
 PIN 3 bpWR  
 PIN 4 bsOUT  
 PIN 5 bsINP  
 PIN 6 bsMWRT  
 PIN 7 bsMEMR  
 PIN 8 DUAL  
 PIN 9 ISA\_SEL  
 PIN 10 W\_LOW  
 PIN 11 W\_HIGH  
 PIN 13 R\_LOW  
 PIN 14 R\_HIGH  
 PIN 15 RESET

----- OUTPUT SIGNALS -----

PIN 16 NC ;<--- LOW if two back to back 8 bit read/writes  
 PIN 17 NC  
 PIN 18 GAL\_INT ;Pass an ISA bus INT to S100 bus (not use here)  
 PIN 19 FF\_CLEAR  
 PIN 20 ISA\_MEMW ;Main ISA memory write signal  
 PIN 21 ISA\_IOR ;Main ISA I/O read signal  
 PIN 22 ISA\_MEMR ;Main ISA memory read signal  
 PIN 23 ISA\_IOW ;Main ISA I/O write signal

----- Boolean Equation Segment -----  
 EQUATIONS

/FF\_CLEAR = /RESET ;Reset the 74LS74  
 ;ISA MEM Writes  
 ;RAM WR8  
 /ISA\_MEMW = bsMWRT \* ISA\_SEL \* /bpWR \* /DUAL  
 + bsMWRT \* /bpWR \* /W\_LOW  
 + bsMWRT \* /bpWR \* /W\_HIGH  
 ;ISA MEM Reads  
 /ISA\_MEMR = bsMEMR \* ISA\_SEL \* bpDBIN \* /DUAL  
 + bsMEMR \* bpDBIN \* DUAL  
 ;RAM RD8  
 /ISA\_IOW = bsOUT \* ISA\_SEL \* /bpWR \* /DUAL  
 + bsOUT \* /bpWR \* /W\_LOW  
 + bsOUT \* /bpWR \* /W\_HIGH  
 ;ISA I/O Writes  
 ;I/O WR8  
 /ISA\_IOR = bsINP \* ISA\_SEL \* bpDBIN \* /DUAL  
 + bsINP \* bpDBIN \* DUAL  
 ;ISA I/O Reads  
 ;I/O RD8