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TITLE    Driver select signals for U13, U14, U22 & U23.
PATTERN
REVISION 0
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COMPANY
DATE     08/16/14

CHIP DUAL_8  PALCE22V10                        ; Device not selected

;---------------------------------- PIN Declarations ---------------

                                               ;----- INPUT SIGNALS -------

PIN 1   DUAL                                   ;High if back to back 8 bit bytes required
PIN 2   W_LOW                                  ;S100 low byte write, From ISA_SEL.GAL
PIN 3   W_HIGH                                 ;S100 high byte write, From ISA_SEL.GAL
PIN 4   R_LOW                                  ;S100 low byte read, From ISA_SEL.GAL
PIN 5   R_HIGH                                 ;S100 high byte read, From ISA_SEL.GAL

PIN 6   bpDBIN                                 ;S100 bus CPU read
PIN 7   bpWR                                   ;S100 bus CPU write
PIN 8   ISA_SEL                                ;ISA board select

PIN 9   RD8                                    ;Output from ABC_PAL (Currently Unused)
PIN 10  RD16                                   ;Output from ABC_PAL (Active LOW)
PIN 11  WR8                                    ;Output from ABC_PAL (Currently Unused)
PIN 13  WR16                                   ;Output from ABC_PAL(Currently Unused)
PIN 14  GAL_A0                                 ;ISA A0 line
PIN 15  SHIFT_X                                ;To delay U13 LE & OE (use P15 15-16, & K3 1-2)

                                               ;----- OUTPUT SIGNALS --------
                                               ;For single/dual back to back 8 bit transfers

PIN 16  U13_LE                                 ;U13 latch Enable (From CPU,LOW byte to ISA)
PIN 17  U14_LE                                 ;U14 latch Enable (To CPU LOW byte from ISA)
PIN 18  U22_LE                                 ;U22 latch Enable (From CPU, HIGH byte to ISA)
PIN 19  U23_LE                                 ;U23 latch Enable (To CPU, HIGH byte from ISA)

PIN 20  U13_OE                                 ;U13 output Enable
PIN 21  U14_OE                                 ;U14 output Enable
PIN 22  U22_OE                                 ;U22 output Enable
PIN 23  U23_OE                                 ;U23 output Enable

;---------------------------------- Boolean Equation Segment ------

EQUATIONS

U14_LE          =       DUAL * /RD16  * /GAL_A0                ;First of 2 bytes READ
/U14_OE         =       DUAL * /RD16

U23_LE          =       DUAL * /RD16  *  GAL_A0                ;Second byte READ
/U23_OE         =       DUAL * /RD16



U13_LE          =       DUAL * /WR16  * /GAL_A0                ;First of 2 bytes WRITE to 74LS374
/U13_OE =               DUAL * /WR16  * /GAL_A0

U22_LE          =       DUAL * /WR16  * GAL_A0                 ;Second byte
/U22_OE =               DUAL * /WR16  * GAL_A0
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