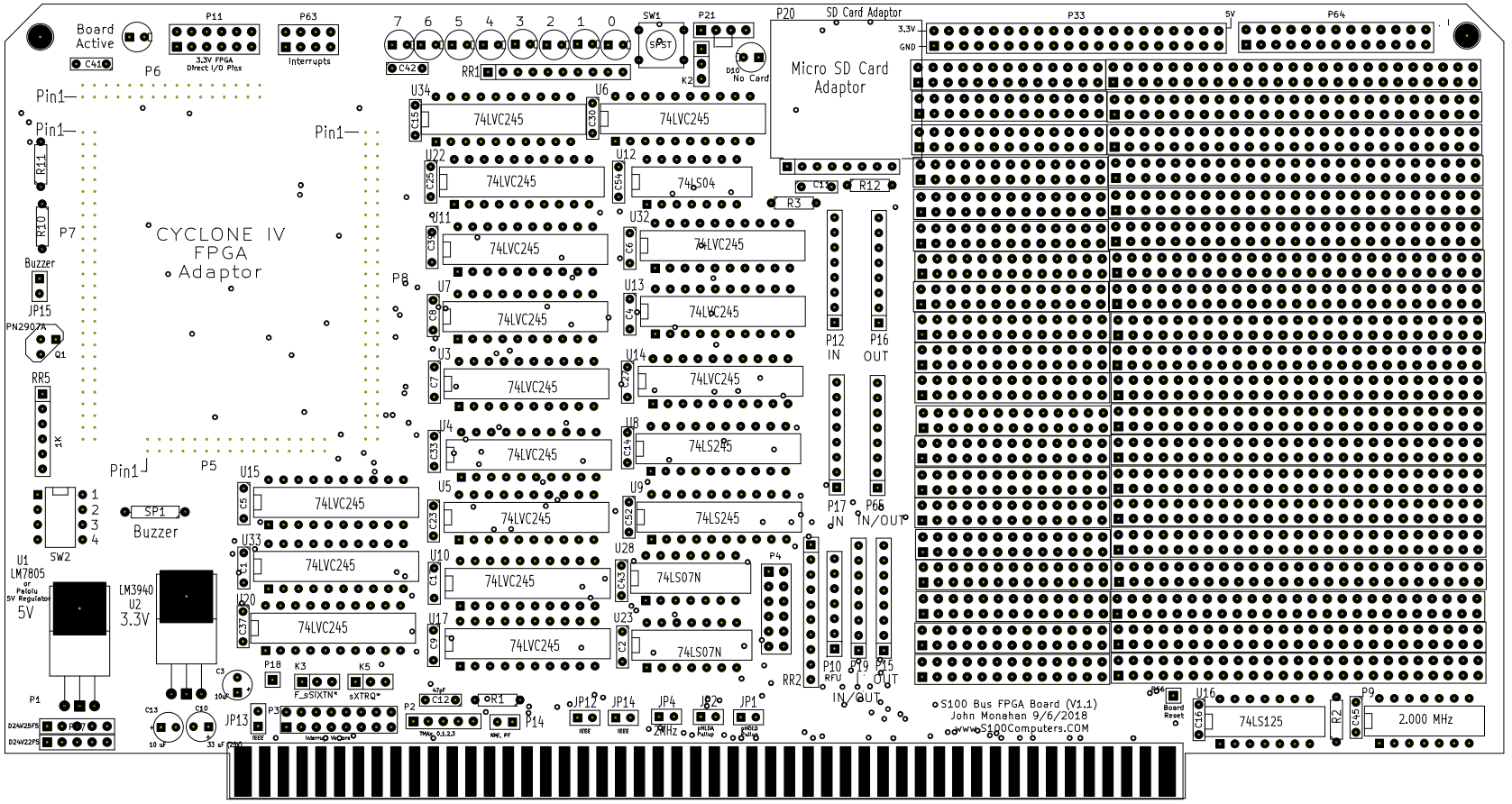

Board Active
P11
3.3V FPGA Direct I/O Pins
P63
Interrupts
P6
Pin1
CYCLONE IV FPGA Adaptor
P7
Buzzer
JP15
PN2907A
Q1
RR5
1K
P5
SP1
Buzzer
SW2
U1
LM7805
or
Palolu
5V Regulator
5V
LM3940
U2
3.3V
P1
D24V25F5
D24V22F5
P18
K3
K5
F_sSIXTN*
sXTRQ*
JP13
P3
P2
P14
P12
JP14
JP4
JP2
JP1
2MHz
P20
SD Card Adaptor
Micro SD Card Adaptor
SW1
SPST
P21
K2
D10
No Card
RR1
U34
U6
74LVC245
U22
U12
74LS04
U11
U32
U7
U13
U3
U14
U4
U8
74LS245
U5
U9
U15
U33
U10
U28
74LS07N
U20
U17
U23
P4
RR2
P12
IN
P16
OUT
P17
IN
P66
IN/OUT
P10
RFU
P19
IN/OUT
P15
OUT
P33
3.3V
GND
5V
P64
S100 Bus FPGA Board (V1.1)
John Monahan 9/6/2018
www.S100Computers.COM
Board Reset
U16
74LS125
R2
P9
2.000 MHz