BOARD RAM SELECT LED

16MG_RAM_SELECT
16MG_RAM_SELECT*
PHI
8rd
16rd
8wr
16wr
GND
GND
U28 74LS240
470 R1
D1 LED
16MG_RAM_SELECT
PHI*
PX1
PX2
PX3
PX4

PUA1 C5 10 uF
PUA2 C6 10 uF
PUA3 C31 10 uF
PUA4 C32 10 uF
U7A 74LS123
U7B 74LS123
U16A 74LS123
U16B 74LS123
8rd
16rd
8wr
16wr
D2 D3 D4 D5
PUB1 PUB2 PUB3 PUB4 PUB5 PUB6 PUB7 PUB8

U23 U24 U2 74LS244
bA1 bA2 bA3 bA4 bA5 bA6 bA7 bA8 bA9 bA10 bA11 bA12 bA13 bA14 bA15 bA16 bA17 bA18 bA19 bA20 bA21 bA22 bA23

U19 U31 U22 74LS245
DI0 DI1 DI2 DI3 DI4 DI5 DI6 DI7
OD0 OD1 OD2 OD3 OD4 OD5 OD6 OD7
ED0 ED1 ED2 ED3 ED4 ED5 ED6 ED7
sMEMR* X* Y* Z*

P1
+8V +16V XRDY VIO* VI1* VI2* VI3* VI4* VI5* VI6* VI7* NMI* PWRFAIL* TMA3* A18 A16 A17 SDSB* CDSB* 0V_A NDEF1 ADSB* DODSB* PHI pSTVAL* pHLDA RFU1 RFU2 A5 A4 A3 A15 A12 A9 DO1_ED1 DO0_ED0 A10 DO4 DO5 DO6 DI2 DI3 DI7 sM1 sOUT sINP sMEMR sHLTA CLOCK 0V +8V −16V 0V_B SLAVE_CLR* TMA0* TMA1* TMA2* sXTRQ* A19 SIXTN* A20 A21 A22 A23 NDEF2 NDEF3 PHANTOM* MWRT RFU3 0V_C RFU4 RDY INT* HOLD* RESET* pSYNC pWR* pDBIN A0 A1 A2 A6 A7 A8 A13 A14 A11 DO2_ED2 DO3_ED3 DO7_ED7 DI4_OD4 DI5_OD5 DI6_OD6 DI1_OD1 DI1_OD1 sINTA sWO* ERROR* POC* 0V
S100_MALE

JP1 JP2 JP3 JUMPER

IC2 IC3 P49 P50 P51 P52 P53 P54 CONN_8
SW3 SW4 DIPS_08
74LS682 74LS682N
LOWER_SELECT

K3 Jumper
1-2 AS6C3216
2-3 CY62167
CONN_3 K3
U4A 74LS139

U25A 74LS32 U25B 74LS32 U25C 74LS32
sOUT sINP sINTA IO_REQUEST

U9 74LS240
pDBIN sXTRQ* pWR* A0 sMEMR
pDBIN* sXTRQ pWR A0* A0** sMEMR*

U12A 74F11 U12B 74F11 U12C 74F11 U13A 74F11
U30A 74F27 U10A 74LS01 U30B 74F27 U17A 74F08 U17B 74F08 U14A 74F08 U14C 74F08 U14B 74F08 U14D 74F08
U30C 74F27 U15A 74F02 U15B 74F02 U15C 74F02 U15D 74F02
JP8 JUMPER
SIXTN* X* Z* Y* E* F* G* H*

U10D 74LS01
PHANTOM* RFU3

RR1 RR2 RR3 4700 1000
PUC1 PUC2 PUC3 PUC4
PUA1 PUA2 PUA3 PUA4
PUB1 PUB2 PUB3 PUB4 PUB5 PUB6 PUB7 PUB8

5.0v
3.3v
VCC VDD
PWR_FLAG
BANK 1
BANK 2
U5 U6 U8 U11
AS6C3216
bA1 bA2 bA3 bA4 bA5 bA6 bA7 bA8 bA9 bA10 bA11 bA12 bA13 bA14 bA15 bA16 bA17 bA18 bA19 bA20 bA21 bA22
OD0 OD1 OD2 OD3 OD4 OD5 OD6 OD7
ED0 ED1 ED2 ED3 ED4 ED5 ED6 ED7
E* G* F* H*
PUC1 PUC2 PUC3 PUC4
P2 P3 CONN_4X2
A* B* C* D*

Jumper
1-2 AS6C3216 (0-16MB)
1-2 CY62167 (8-16MB)
5-6 AS6C3216 (0-16MB)
3-4 CY62167 (8-16MB)
7-8 CY62167 (8-16MB)

SPARES
U17D 74F08 U17C 74F08 U13C 74F11 U13B 74F11 U25D 74LS32 U4B 74LS139

C7 C8 C9 C10 C11 C12 C13 C14 C15 C16 C17 C18 C19 C21 C22
C23 C24 C25 C26 C27 C28 C29 C30 C36 C38 C39 C40 C41 C42 C43
0.1 uF

LM7805 LM3940 U1 U3
C1 0.33 uF C3 0.1 uF C2 10 uF C4 10 uF
+8V VCC 0V VDD
Vss GND

Card Ejector Mounting Holes
P35 P34

File: s100_16MB SRAM-V3.sch	
Sheet: /	
Title: 16MB SRAM PROTOTYPE V2	
Size: C	Date: 29 oct 2014
KiCad E.D.A.	eeschema (2013-07-07 BZR 4022)-stable
Rev:	Id: 1/1